# FROM ZERO TO HERO: THE TEMPERATURE WAR



#### WHO AM I?



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## INTRODUCTION

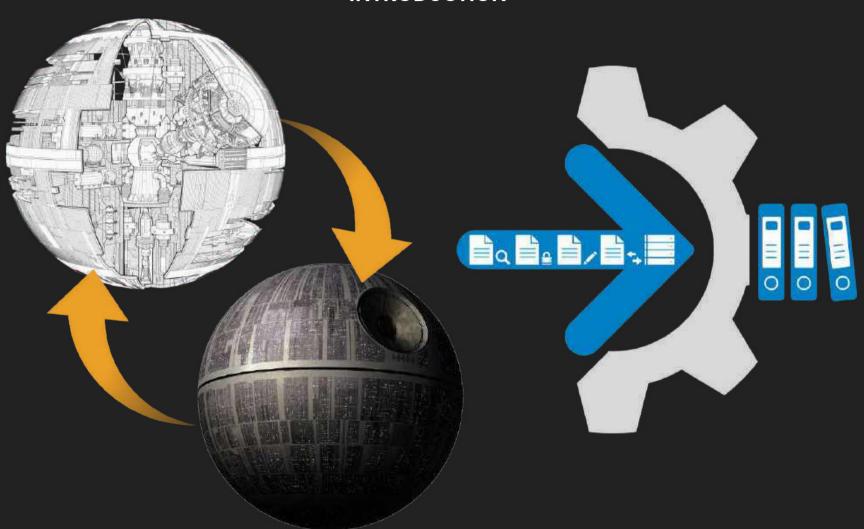
**PURPOSE** 

**SYSTEMS ENGINEERING** 

LIFE CYCLE: V-MODEL

**PROCESSES** 

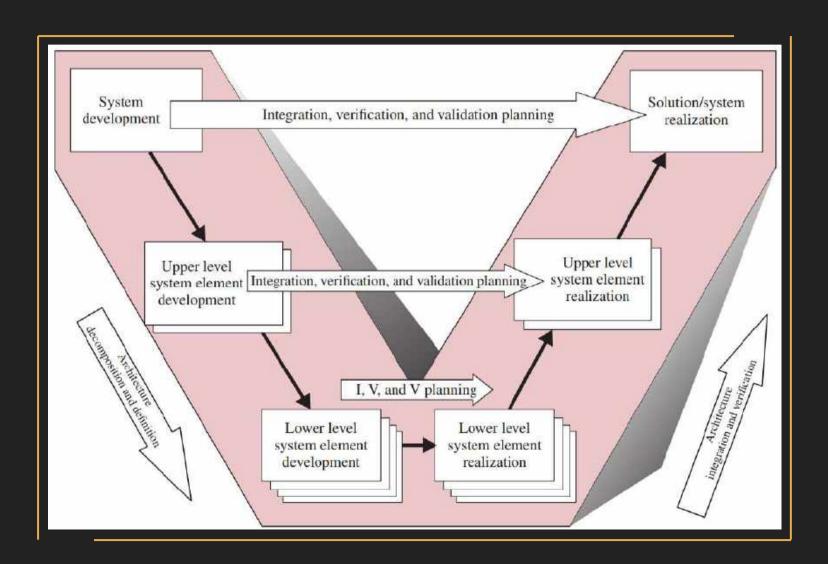
### **PURPOSE**



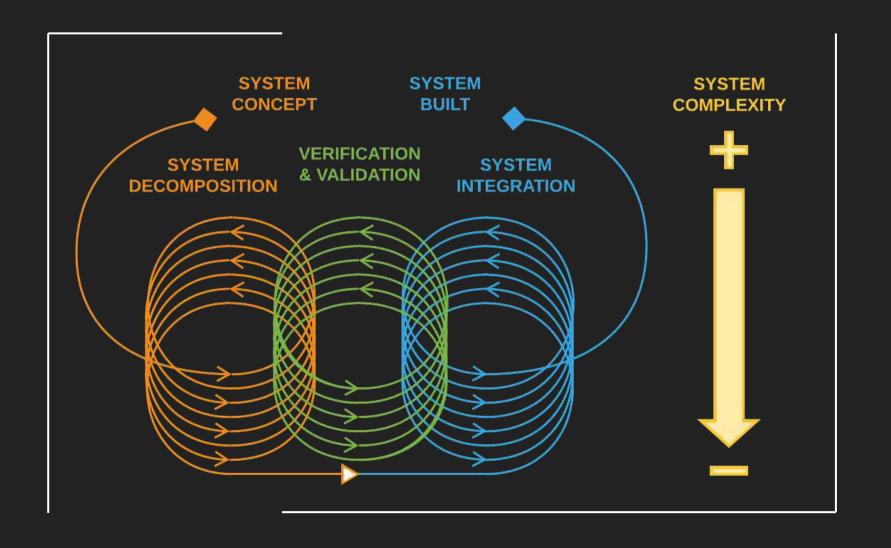
### **SYSTEMS ENGINEERING**



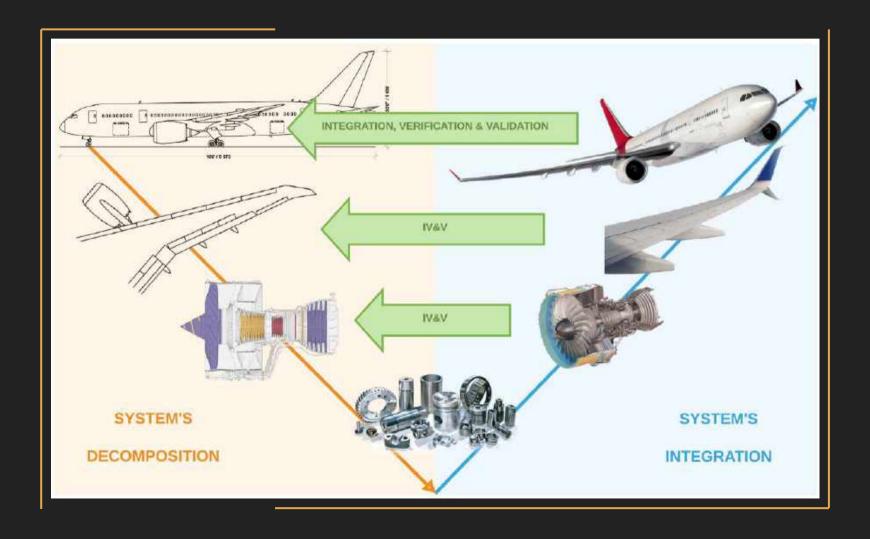
#### LIFE CYCLE: V-MODEL



#### LIFE CYCLE: V-MODEL



#### **LIFE CYCLE: V-MODEL**



#### **PROCESSES**

#### INTRODUCTION

TECHNICAL PROCESSES		TECHNICAL MANAGEMENT PROCESSES	AGREEMENT PROCESSES	ORGANIZATIONAL PROJECT-ENABLING PROCESSES
Business or mission analysis process	Integration process	Project planning process	Acquisition Process	Life cycle model management process
Stakeholder needs and requirements definition process	Verification process	Project assessment and control process	Supply Process	Infrastructure management process
System requirements definition process	Transition process	Decision management process		Portfolio management process
Architecture definition process	Validation process	Risk management process		Human resource management process
Design definition process	Operation process	Configuration management process		Quality management process
System analysis process	Maintenance process	Information management process		Knowledge management process
Implementation process	Disposal process	Measurement process		
		Quality assurance process	<u> </u>	

#### **CORE PROCESSES**

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### STAKEHOLDER NEEDS

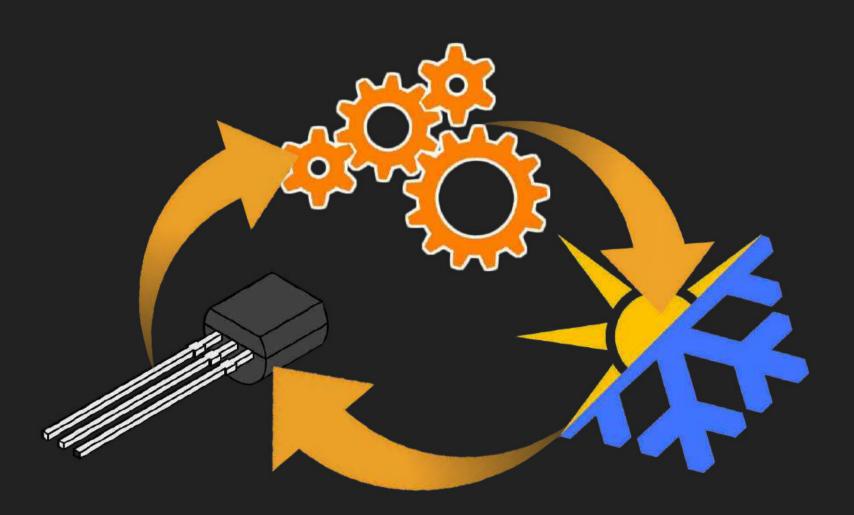
**GENERAL NEEDS** 

**INITIAL SOW** 

**SOW VERIFICATION** 

### **GENERAL NEEDS**

#### **STAKEHOLDER NEEDS**



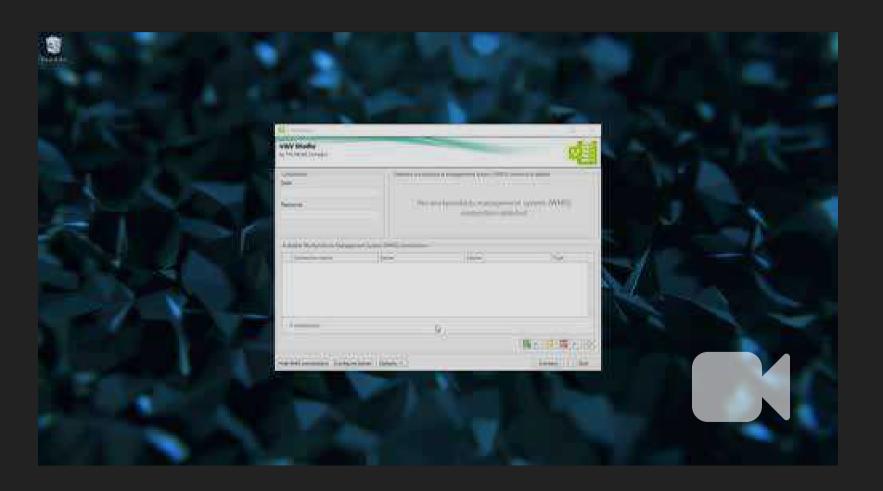
#### **INITIAL SOW**

#### STAKEHOLDER REQUIREMENTS



### **SOW VERIFICATION**

#### **STAKEHOLDER NEEDS**



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### **OPERATIONAL ANALYSIS**

STAKEHOLDER REQUIREMENTS

**GOALS & STKR TRACEABILITY** 

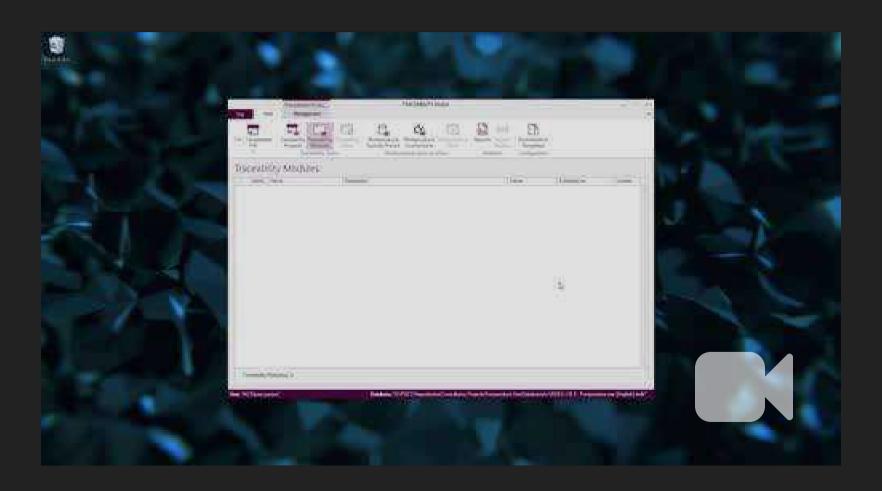
### STAKEHOLDER REQUIREMENTS

#### **OPERATIONAL ANALYSIS**



### **GOALS & STKR TRACEABILITY**

#### **OPERATIONAL ANALYSIS**



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### SYSTEM CAPABILITIES

**SYSTEM REQUIREMENTS** 

**QUALITY ANALYSIS SYSR** 

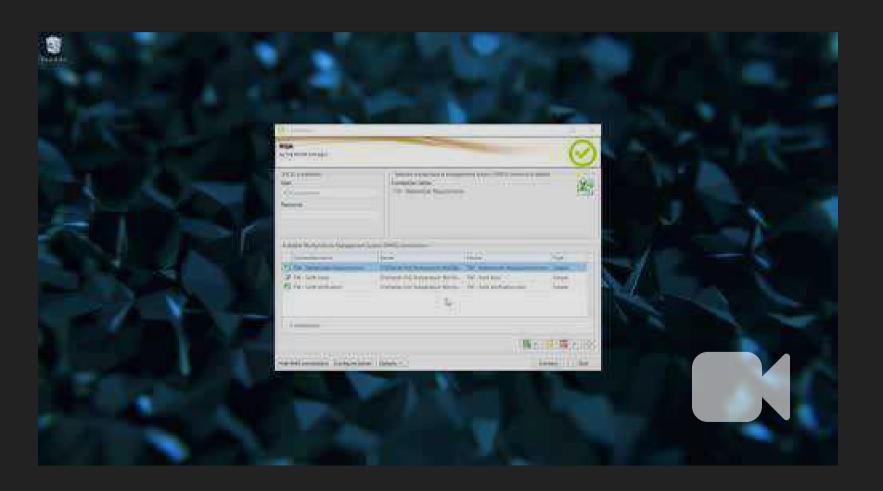
### **SYSTEM REQUIREMENTS**

#### **SYSTEM CAPABILITIES**



### **QUALITY ANALYSIS SYSR**

#### **SYSTEM CAPABILITIES**



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### SYSTEM ARCHITECTURE

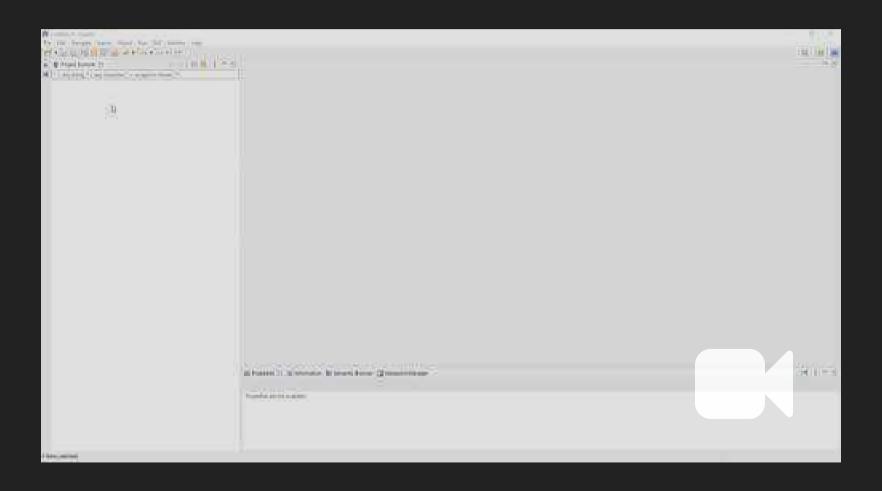
**MODELS GENERATION** 

**SUBSYSTEM REQUIREMENTS** 

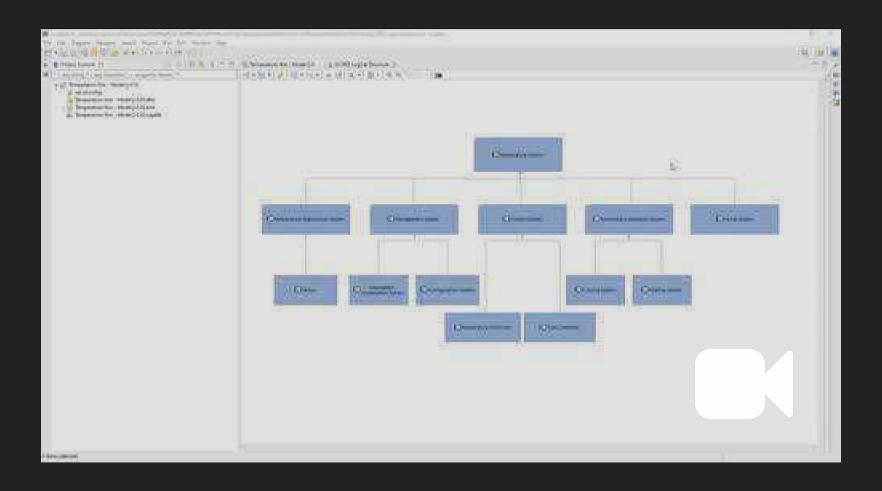
REQUIREMENTS SYNCHRONIZATION

**SYSR & SUBSYSR TRACEABILITY** 

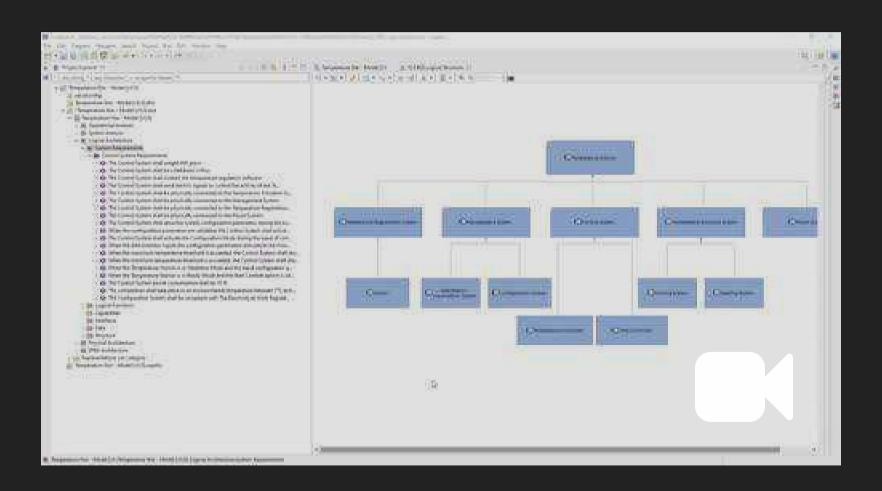
### **MODEL GENERATION**



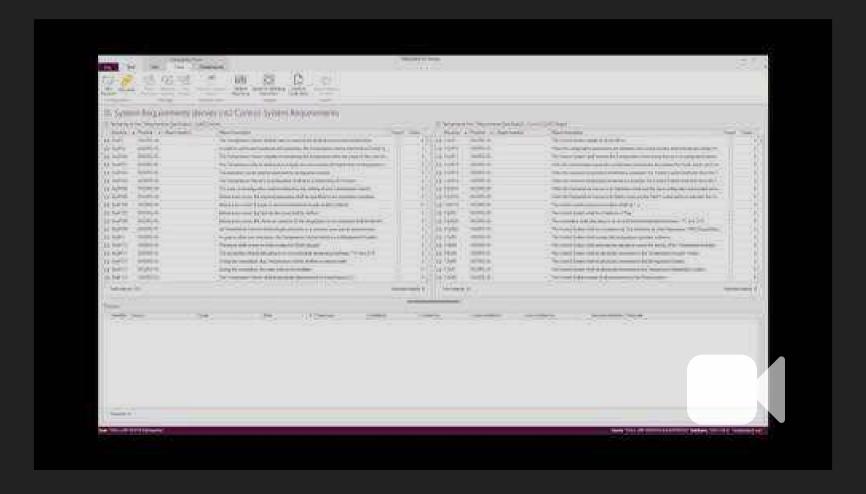
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#### **REQUIREMENTS SYNCHRONIZATION**



#### **SYSR & CSYSR TRACEABILITY**



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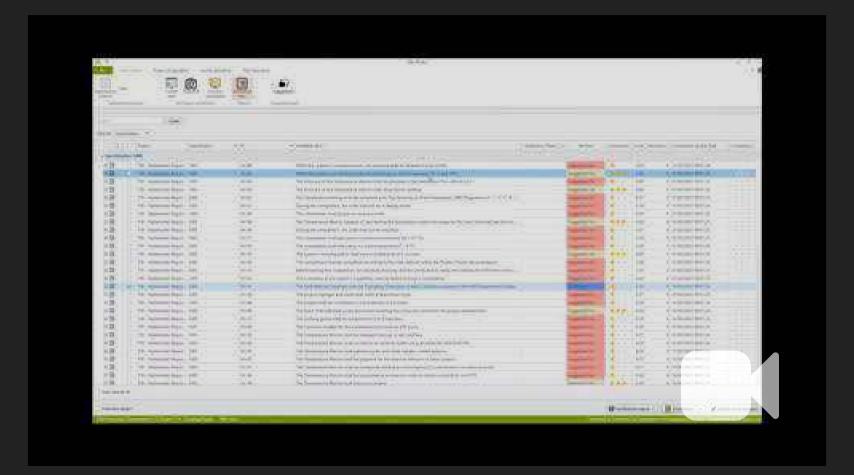
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### **VERIFICATION & VALIDATION**

#### **SIMULATION**

#### **SIMULATION**

#### **VERIFICATION & VALIDATION**



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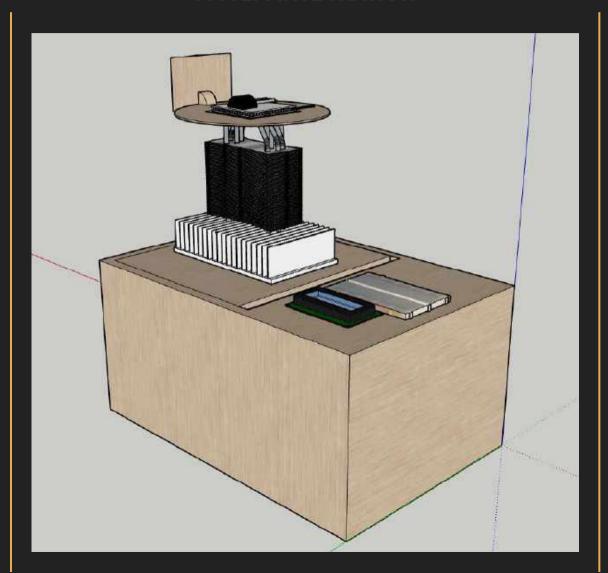
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### **SYSTEM INTEGRATION**

**3D MODEL** 

### **3D MODEL**

#### **SYSTEM INTEGRATION**



### **3D MODEL**

#### **SYSTEM INTEGRATION**



# QUESTIONARY





# THANK YOU FOR YOUR

# TIME AND ATTENTION

